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**JAIPUR**

**INDIVIDUAL FACULTY DATA SHEET**

|  |  |  |
| --- | --- | --- |
| **Name of the faculty member** | **:** | Deepak sankhala |
| **Present Designation** | **:** | Asst.Professor |

**Email Id: Deepak.ece@jecrc.ac.in**

**Contact Details:**

**I. Particulars of Educational Qualification: (*only completed*)**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Category** | **Name of the Degree** | **Specialization** | **Year of Passing** | **Name of the College** | **Name of the University** |
| **UG** | **B.E** | **Electronics and comunication** | **2007** | **M.L.V.T.Govt.Engineering college** | **Rajasthan university** |
| **PG** | **M.Tech** | **Electronics communication** | **2013** | ------- | JNU University Jaipur |
| **Ph.D.** |  |  |  |  |  |

**II. Academic Experience : 8**

**III. Industrial Experience :0**

**IV. Title of Ph.D. Thesis :0**

**V. Awards/Achievements : 0**

**VI. Research/Specialization : 0**

**VII. Course Taught :** Electronic Devices & Circuits, Circuit Analysis & Synthesis, Analog Electronics, Analog Communication, Telecommunication Engg. Digital Communication, Wireless Communication, Antenna & Wave Propagation

**VIII. Refereed International Journal Publication : (1) FPGA Implementation of less power FIR Filters**

**(2)** MATCHED FILTER DESIGN MODEL WITH

LESS POWER CONSUMPTION

**(3) DESIGNING TECHNIQUE OF THE H-SHAPE U**

**SLOTTED MICROSTRIP PATCH ANTENNA**”

**IX. International Conference Proceeding :1**

**X. Books Written : 0**

**XI. M.Tech Thesis Guide : 0**

**XII. PHD Thesis Guide :0**

**XIII. Professional Body Membership : 0**